

DUAL LOOP FEEDFORWARD POWER AMPLIFIER

ABSTRACT OF THE DISCLOSURE

A feedforward amplifier. The present invention therefore
5 provides a feedforward amplifier, comprising: a pilot reference
generator that generates a pilot signal; a first carrier
cancellation loop; a first error amplifier loop including a first
error amplifier, wherein the pilot signal is utilized to provide
gain stabilization in the first error amplifier a third loop; a
10 second carrier cancellation loop that includes a first phase and
amplitude adjuster in module and a second amplitude and phase
adjuster disposed between module and module, wherein the pilot
signal is utilized to control the adjustment of the second carrier
cancellation loop.

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